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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. US 7,333,362 B2 )  
GENDRIER ET AL. )

Issued: FEBRUARY 19, 2008 )

For: ELECTRICALLY ERASABLE AND )  
PROGRAMMABLE, NON-VOLATILE SEMI- )  
CONDUCTOR MEMORY DEVICE HAVING A )  
SINGLE OF GATE MATERIAL, AND )  
CORRESPONDING MEMORY PLANE )

**Certificate**  
AUG 06 2008  
**of Correction**

REQUEST FOR CERTIFICATE OF CORRECTION OF  
OFFICE MISTAKE UNDER RULE 1.322(a)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of Rule 1.322(a) of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO-1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent which are ordered in the future.

The corrections being made are of consequence to provide the proper recitation in the claims. Moreover, since these errors are believed to be errors of the Patent Office, such

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Correction

AUG 6 2008

In re Patent of

**GENDRIER ET AL.**

Patent No. **US 7,333,362 B2**

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changes should be made at no expense to the Patentee. No new matter is added by these corrections.

Respectfully submitted,

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on this 1st day of ~~July~~, 2008.

-August

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| UNITED STATES PATENT AND TRADEMARK OFFICE  |   |
|--|---|
| CERTIFICATE OF CORRECTION  |   |
| PATENT NO.   | : US 7, 333,362 B2                                    |
| DATED  | : February 19, 2008                                   |
| INVENTOR(S)  | : Gendrier et al.                                     |
| It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below |   |
| Column 16, Line 65   | Delete: " 31"<br>Insert: -- 1 --                      |
| Column 17, Line 46   | Delete: " disposed a "<br>Insert: -- disposed in a -- |
| Column 20, Line 17   | Delete: " transistor "<br>Insert: -- transistors --   |

MAILING ADDRESS OF SENDER:

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